

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,934,458 B2
APPLICATION NO. : 10/764732
DATED : August 23, 2005
INVENTOR(S) : Brown et al.

Page 1 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Column 6, Line 1, delete "electrothermal" and insert -- electro-thermal --, therefor.

In Column 6, Line 6, after "circuit 10" delete "and".

In Column 6, Line 7, after "SW1," delete "is".

In Column 6, Line 15, delete "E1," and insert -- E₁ --, therefor.

In Column 6, Line 26, delete "E2" and insert -- E₂ --, therefor.

In Column 6, Line 51, delete "T1." and insert -- T₁ --, therefor.

In Column 11, Line 41, delete "OFT)" and insert -- (FFT) --, therefor.

In Column 12, Line 33, delete "n-network" and insert -- π -network --, therefor.

In Column 13, Line 35, delete "MHZ." and insert -- MHz --, therefor.

In Column 13, Line 50, delete "sec" and insert -- μ sec --, therefor.

In Column 14, Line 47, delete "(FETE)." and insert -- (FET1). --, therefor.

In Column 15, Line 8, delete "MOSFES" and insert -- MOSFET --, therefor.

In Column 15, Line 25, delete "13" and insert -- I₃ --, therefor.

In Column 16, Line 2, delete "MHZ" and insert -- MHz --, therefor.

In Column 16, Line 55, delete "t_S" and insert -- t_s --, therefor.

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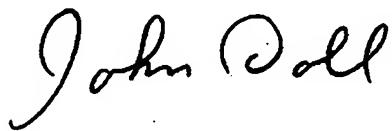
Page 2 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Column 18, Line 51, delete "MHZ," and insert -- MHz, --, therefor.

Signed and Sealed this

Twenty-fourth Day of February, 2009



JOHN DOLL
Acting Director of the United States Patent and Trademark Office